SAMANTA CHANDRASEKHAR INSTITUTE OF TECHNOLOGY & MANAGEMENT

SEMILIGUDA-764 036, KORAPUT

DEPT. OF CSE (Odd Sem.)

Sept 22 to Jan-2023

LESSON PLAN AND PROGRESS REGISTER

(To be maintained by all members of the teaching staff)

SESSION 2022-23

NAME Kalpataru Das

DESIGNATION H.O.D

DEPT. CSE

SIGNATURE

SEMESTER WISE (1st, 3rd, 5th, 7th) TIME TABLE FOR YOUR BRANCH (First Half)

(Mention time for the period, Course No. & Room No.) Degree / Diploma

Period / Days	1st 9.15-10.00	2nd 10.00-10.45	3rd 11.00-12.00	4th 12.00-1.00	5th 1.00-2.00	6th 2.00-3.00	7th 3.00-4.00	8th	9th	10th
Monday	Karate	DS	EVS	DEC	L	OOM	CSA			
Tuesday	DS Lab	DS Lab	→	CSA	U	EVS	OOM			
Wednesday	DE	OOM Lab	→	EVS	N	OOM	DS			
Thursday	OOM	DE Lab	→	EVS	C	CSA	DS			
Friday	DE	OOPS Lab	→	DS	H	OOM	Swatch Bharat			
Saturday	CSA	DS	DE	EVS	↓	Sports →				

5th Sem

SEMESTER WISE (2nd, 4th, 6th, 8th) TIME TABLE FOR YOUR BRANCH (Second Half)

(Mention time for the period, Course No. & Room No.) Degree / Diploma

Period / Days	1st	2nd	3rd	4th	5th	6th	7th	8th	9th	10th
Monday	Karate	INT	SE	CHM	L	EMST	MC			
Tuesday	MC	EMST	INT	Lib	U	SE	CHM			
Wednesday	CPRO Eng.	INT	WD	LAB	N	CHM	SE			
Thursday	EMST	MC	CHM	Lab	C	ESEA →	INT			
Friday	SE	EMST	PPL	Lab	H	INT	Swatch Bharat			
Saturday	CHM	SW	Project →			ES Sports →				

This Lesson Plan and Progress Register is to be submitted to the Director for verification and counter signature twice in every semester. The H.O.D. must verify and sign this Register before submission.

COURSES ALLOTED

FOR DIFFERENT BRANCHES & SEMESTER (Degree/Diploma/+2 Science)

Semester	Course No.	Course Title
3rd	Th-1	Computer System Architecture
5th	Th-4	Computer H/W and maintenance.

N.B. : Submission of Annual Lesson Plan-cum-Progress Register and performance Report for Assessment are responsibility of each faculty member.

LESSON PLAN

Degree/Diploma/+2 Science (Theory/Pract/Lab/Workshop)

Semester 3rd Branch CSE

Month & Date	Course No. & Title	Brief note of the topics to be covered	No. of Classes Required
15/9/22	1. Basic Structure of computer hardware	Basic Structure of Computer hardware	01
19/9/22		Functional Units	01
20/9/22		Computer Components	01
22/9/22		Performance Measures	01
24/9/22		Memory Addressing	01
26/9		Memory Operations	01
27/9	2. Instruction & Instruction sequencing	Fundamentals to Instructions	01
29/9		Operands	01
1/10		op codes	01
8/10		Instruction Formats	01
10/10		Addressing Modes	01
11/10	3. Processor systems	Register Files	01
13/10		Complete Instruction Execution	01
15/10		Fetch Decode	01
17/10		Execution	01

PROGRESS

Degree/Diploma/+2 Science (Theory/Pract/Lab/Workshop)

Semester 3rd Branch CSE

Date	Course No. & Title	No. of Student Present	Mention the Topics covered	If not taken mention the reasons	Remarks/ Signature of HOD/Director
15/9/22	1. Basic Structure of computer hardware	21	Basic Structure of Computer hardware		
19/9		22	Functional units		
20/9		22	Computer components		
22/9		20	Performance Measures		
24/9		15	Memory Operations		
26/9	2. Instruction & Instruction sequencing	17	Fundamentals to Instructions		
27/9		17	Operands		
29/9		20	Op Codes		
1/10		19	Instruction formats		
8/10		13	Addressing Modes		
10/10	3. Processor Systems	16	Register Files		
11/10		20	complete Instruction Execution		
13/10		17	Fetch		
15/10		16	Decode		
17/10		15	Execution		

LESSON PLAN

Degree/Diploma/+2 Science (Theory/Pract/Lab/Workshop)

Semester 3rd Branch

Month & Date	Course No. & Title	Brief note of the topics to be covered	No. of Classes Required
18/10		Hardware Control	01
20/10		Hardware Control	01
22/10		Micro Program Control	01
27/10		Micro Program Control	01
29/10	4. Memory system	Memory Characteristics	01
31/10		Memory hierarchy	01
1/11		RAM Organization	01
3/11		ROM Organization	01
5/11		Interleaved Memory	01
7/11		Cache Memory	01
10/11		Virtual Memory	01
12/11		Revision	01
14/11	5.	Input Interface	01
15/11	Input-output system	Output Interface	01
19/11		Modes of Data Transfer	01

PROGRESS

Degree/Diploma/+2 Science (Theory/Pract/Lab/Workshop)

Semester Branch

Date	Course No. & Title	No. of Student Present	Mention the Topics covered	If not taken mention the reasons	Remarks/ Signature of HOD/Director
18/10		19	Hardware Control		
20/10		17	Hardware control		
22/10		13	Micro program control		
27/10		17	Micro program Control		
29/10	4. Memory System	15	Memory Charact-eristics		
31/10		19	Memory hierarchy		
1/11		16	RAM organization		
3/11		15	ROM Organization		
5/11		14	Interleaved Memory		
7/11		18	Cache Memory		
10/11		15	Virtual Memory		
12/11		19	Revision		
14/11	5. Input Output System	22	Input Interface		
15/11		18	Output Interface		
19/11		15	Modes of Date Transfer		

LESSON PLAN

Degree/Diploma/+2 Science (Theory/Pract/Lab/Workshop)

Semester 3rd Branch CSE

Month & Date	Course No. & Title	Brief note of the topics to be covered	No. of Classes Required
21/11		Programmed I/O Transfer	01
22/11		Interrupt Driven I/O	01
24/11		DMA	01
26/11		I/O Processor	01
28/11		Revision	01
29/11	6. I/O Interface and Bus Architecture	Bus and System Bus	01
1/12		Types of System Bus • Data	01
3/12		• Address • control	01
5/12		Bus structure	01
6/12		Basic Parameters of Bus Design	01
8/12		SCSI	01
10/12		USB	01
12/12		Revision	01
13/12	7. Parallel	Parallel Processing	01
15/12		Linear Pipeline	

PROGRESS

Degree/Diploma/+2 Science (Theory/Pract/Lab/Workshop)

Semester 3rd Branch CSE

Date	Course No. & Title	No. of Student Present	Mention the Topics covered	If not taken mention the reasons	Remarks/ Signature of HOD/Director
21/11		14	Programmed I/O Transfer		
22/11		18	Interrupt Driven I/O		
24/11		18	DMA		
26/11		18	I/O Processor		
28/11		12	Revision		
29/11	6. I/O Interface and Bus Architecture	13	Bus and System Bus		
1/12		14	Types of System Bus • Date		
3/12		15	• Address • control		
5/12		15	Bus Structure		
6/12		16	Basic parameters of Bus Design		
8/12		19	SCSI		
10/12		15	USB		
12/12		16	Revision		
13/12	7. Parallel	17	Parallel processing		
15/12		18	Linear pipeline		